

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hideki IWAKI

Application No. 09/994,683

Filed: November 28, 2001

MULTI-LEVEL CIRCUIT SUBSTRATE, For:

METHOD FOR MANUFACTURING

SAME AND METHOD FOR

ADJUSTING A CHARACTERISTIC

IMPEDANCE THEREFOR

Confirmation No. 8158

.TC/Art Unit: 2826

Examiner: Leonardo Andujar

Atty Docket: P94916US1

LETTER RESPONDING TO COMMUNICATION

MAIL STOP ISSUE FEE Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Communication dated May 27, 2004, please cancel the sheet of drawings containing Figure 14 and the duplicate copy of Figure 9.